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Bib Data Sheet

CONFIRMATION NO. 7374

SERIAL NUMBER 10/829,527	FILING DATE 04/21/2004  RULE	CLASS 326	GROUP ART UNIT 2819	ATTORNEY DOCKET NO. 1207.P004XC6
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APPLICANTS

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**\*\* CONTINUING DATA \*\*\*\*\***

This application is a CON of 10/428,724 05/01/2003 PAT 6,747,482  
 which is a CON of 10/117,875 04/05/2002 PAT 6,597,196  
 which is a CON of 09/482,149 01/12/2000 PAT 6,462,578  
 which is a CON of 08/909,928 08/12/1997 PAT 6,051,991  
 which is a CON of 08/534,500 09/27/1995 ABN  
 which is a CON of 08/229,923 04/14/1994 ABN  
 which is a CIP of 08/101,197 08/03/1993 PAT 5,457,410

*OK Ting*

**\*\* FOREIGN APPLICATIONS \*\*\*\*\***

*none*

IF REQUIRED, FOREIGN FILING LICENSE GRANTED  
**\*\* 06/26/2004**

Foreign Priority claimed 35 USC 119 (a-d) conditions met Verified and Acknowledged	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance Examiner's Signature <i>[Signature]</i> Initials <i>[Initials]</i>	STATE OR COUNTRY CA	SHEETS DRAWING 22	TOTAL CLAIMS 20	INDEPENDENT CLAIMS 24
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TITLE

Architecture and interconnect scheme for programmable logic circuits

☐ All Fees